

IRFR2407PbF IRFU2407PbF

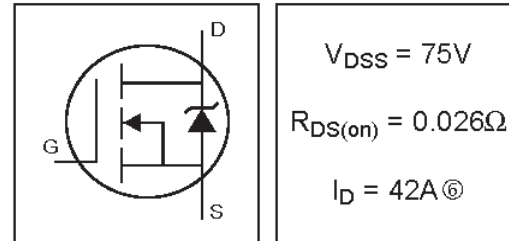
HEXFET® Power MOSFET

- Surface Mount (IRFR2407)
- Straight Lead (IRFU2407)
- Advanced Process Technology
- Dynamic dv/dt Rating
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

Description

Seventh Generation HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The D-Pak is designed for surface mounting using vapor phase, infrared, or wave soldering techniques. The straight lead version (IRFU series) is for through-hole mounting applications. Power dissipation levels up to 1.5 watts are possible in typical surface mount applications.



Absolute Maximum Ratings

| | Parameter | Max. | Units |
|---------------------------------|---|--------------------|-------|
| $I_D @ T_C = 25^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10\text{V}$ | 42@ | A |
| $I_D @ T_C = 100^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10\text{V}$ | 29@ | |
| I_{DM} | Pulsed Drain Current @ | 170 | |
| $P_D @ T_C = 25^\circ\text{C}$ | Power Dissipation | 110 | W |
| | Linear Derating Factor | 0.71 | W/°C |
| V_{GS} | Gate-to-Source Voltage | ± 20 | V |
| E_{AS} | Single Pulse Avalanche Energy @ | 130 | mJ |
| I_{AR} | Avalanche Current @ | 25 | A |
| E_{AR} | Repetitive Avalanche Energy @ | 11 | mJ |
| dv/dt | Peak Diode Recovery dv/dt @ | 5.0 | V/ns |
| T_J | Operating Junction and | -55 to + 175 | °C |
| T_{STG} | Storage Temperature Range | | |
| | Soldering Temperature, for 10 seconds | | |
| | Mounting Torque, 6-32 or M3 screw | 10 lbf•in (1.1N•m) | |

Thermal Resistance

| | Parameter | Typ. | Max. | Units |
|-----------------|----------------------------------|------|------|-------|
| $R_{\theta JC}$ | Junction-to-Case | — | 1.4 | °C/W |
| $R_{\theta JA}$ | Junction-to-Ambient (PCB mount)* | — | 50 | |
| $R_{\theta JA}$ | Junction-to-Ambient | — | 110 | |

* When mounted on 1" square PCB (FR-4 or G-10 Material).
For recommended footprint and soldering techniques refer to application note #AN-994

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|--|--------------------------------------|------|--------|-------|-------|---|
| V _{(BR)DSS} | Drain-to-Source Breakdown Voltage | 75 | — | — | V | V _{GS} = 0V, I _D = 250μA |
| ΔV _{(BR)DSS} /ΔT _J | Breakdown Voltage Temp. Coefficient | — | 0.078 | — | V/°C | Reference to 25°C, I _D = 1mA |
| R _{DS(on)} | Static Drain-to-Source On-Resistance | — | 0.0218 | 0.026 | Ω | V _{GS} = 10V, I _D = 25A ② |
| V _{GS(th)} | Gate Threshold Voltage | 2.0 | — | 4.0 | V | V _{DS} = 10V, I _D = 250μA |
| g _{fs} | Forward Transconductance | 27 | — | — | S | V _{DS} = 25V, I _D = 25A |
| I _{DSS} | Drain-to-Source Leakage Current | — | — | 20 | μA | V _{DS} = 75V, V _{GS} = 0V |
| | | — | — | 250 | | V _{DS} = 60V, V _{GS} = 0V, T _J = 150°C |
| I _{GSS} | Gate-to-Source Forward Leakage | — | — | 200 | nA | V _{GS} = 20V |
| | Gate-to-Source Reverse Leakage | — | — | -200 | | V _{GS} = -20V |
| Q _g | Total Gate Charge | — | 74 | 110 | nC | I _D = 25A |
| Q _{gs} | Gate-to-Source Charge | — | 13 | 19 | | V _{DS} = 60V |
| Q _{gd} | Gate-to-Drain ("Miller") Charge | — | 22 | 34 | | V _{GS} = 10V④ |
| t _{d(on)} | Turn-On Delay Time | — | 16 | — | ns | V _{DD} = 38V |
| t _r | Rise Time | — | 90 | — | | I _D = 25A |
| t _{d(off)} | Turn-Off Delay Time | — | 65 | — | | R _G = 6.8Ω |
| t _f | Fall Time | — | 66 | — | | V _{GS} = 10V ④ |
| L _D | Internal Drain Inductance | — | 4.5 | — | nH | Between lead, 6mm (0.25in.) from package and center of die contact |
| L _S | Internal Source Inductance | — | 7.5 | — | | |
| C _{iss} | Input Capacitance | — | 2400 | — | pF | V _{GS} = 0V |
| C _{oss} | Output Capacitance | — | 340 | — | | V _{DS} = 25V |
| C _{rss} | Reverse Transfer Capacitance | — | 77 | — | | f = 1.0MHz, See Fig. 5 |
| C _{oss} | Output Capacitance | — | 15700 | — | | V _{GS} = 0V, V _{DS} = 1.0V, f = 1.0MHz |
| C _{oss} | Output Capacitance | — | 220 | — | | V _{GS} = 0V, V _{DS} = 60V, f = 1.0MHz |
| C _{oss eff.} | Effective Output Capacitance ⑤ | — | 220 | — | | V _{GS} = 0V, V _{DS} = 0V to 60V |

Source-Drain Ratings and Characteristics

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|-----------------|--|--|------|------|-------|---|
| I _S | Continuous Source Current (Body Diode) | — | — | 42⑥ | A | MOSFET symbol showing the integral reverse p-n junction diode. |
| I _{SM} | Pulsed Source Current (Body Diode) ① | — | — | 170 | | |
| V _{SD} | Diode Forward Voltage | — | — | 1.3 | V | T _J = 25°C, I _S = 25A, V _{GS} = 0V ④ |
| t _{rr} | Reverse Recovery Time | — | 100 | 150 | ns | T _J = 25°C, I _F = 25A |
| Q _{rr} | Reverse Recovery Charge | — | 400 | 600 | nC | di/dt = 100A/μs ④ |
| t _{on} | Forward Turn-On Time | Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D) | | | | |

Notes:

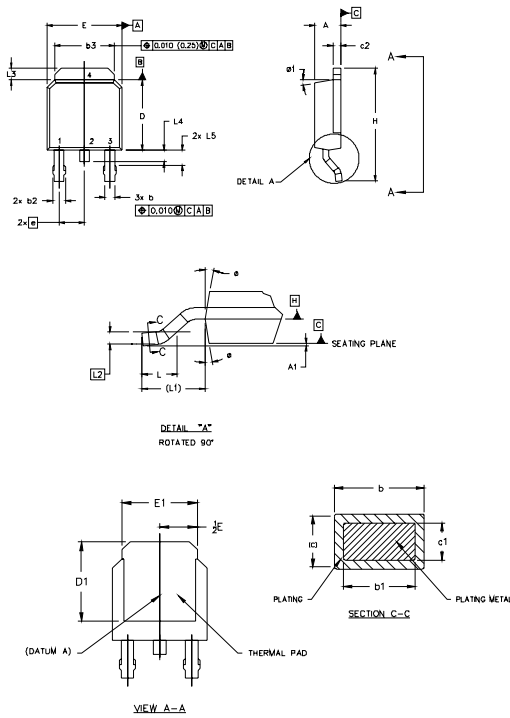
- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting T_J = 25°C, L = 0.42mH, R_G = 25Ω, I_{AS} = 25A.
- ③ I_{SD} ≤ 25A, di/dt ≤ 290A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J ≤ 175°C
- ④ Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ⑤ C_{oss eff.} is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS}
- ⑥ Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 30A

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D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)



- NOTES:
- 1.0 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
 - 2.0 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
 - 3.0 LEAD DIMENSION UNCONTROLLED IN L5
 - 4.0 DIMENSION D1 AND E1 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
 - 5.0 SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND .010 [0.254] FROM THE LEAD TIP.
 - 6.0 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
 - 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.

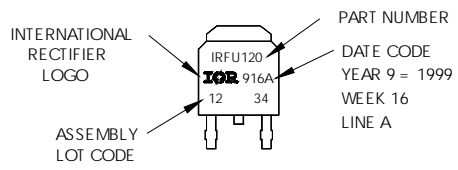
| SYMBOL | DIMENSIONS | | | | NOTES |
|--------|-------------|-------|-----------|-------|-------|
| | MILLIMETERS | | INCHES | | |
| | MIN. | MAX. | MIN. | MAX. | |
| A | 2.18 | 2.39 | .086 | .094 | |
| A1 | | 0.15 | | .005 | |
| b | 0.64 | 0.89 | .025 | .035 | 5 |
| b1 | 0.64 | 0.79 | .025 | 0.031 | 5 |
| b2 | 0.76 | 1.14 | .030 | .045 | |
| b3 | 4.95 | 5.46 | .195 | .215 | |
| c | 0.46 | 0.61 | .018 | .024 | 5 |
| c1 | 0.41 | 0.56 | .016 | .022 | 5 |
| c2 | .046 | 0.89 | .018 | .035 | 5 |
| D | 5.97 | 6.22 | .235 | .245 | 6 |
| D1 | 5.21 | - | .206 | - | 4 |
| E | 6.35 | 6.73 | .250 | .265 | 6 |
| E1 | 4.32 | - | .170 | - | 4 |
| e | 2.29 | | .090 BSC | | |
| H | 9.40 | 10.41 | .370 | .410 | |
| L | 1.40 | 1.78 | .055 | .070 | |
| L1 | 2.74 REF. | | .108 REF. | | |
| L2 | 0.051 BSC | | .020 BSC | | |
| L3 | 0.89 | 1.27 | .035 | .050 | |
| L4 | 1.14 | 1.52 | .045 | .060 | |
| L5 | 1.14 | 1.52 | .045 | .060 | |
| ø | 0" | 10" | 0" | 10" | |
| ø1 | 0" | 15" | 0" | 15" | |

- LEAD ASSIGNMENTS**
- HEXFET**
- 1.- GATE
 - 2.- DRAIN
 - 3.- SOURCE
 - 4.- DRAIN
- IGBTs, CoPACK**
- 1.- GATE
 - 2.- COLLECTOR
 - 3.- EMITTER
 - 4.- COLLECTOR

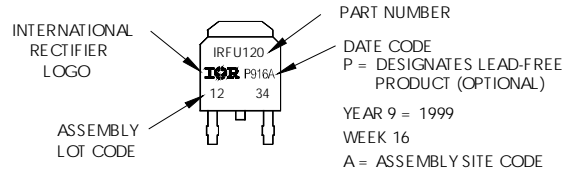
D-Pak (TO-252AA) Part Marking Information

EXAMPLE: THIS IS AN IRFR120 WITH ASSEMBLY LOT CODE 1234 ASSEMBLED ON WW 16, 1999 IN THE ASSEMBLY LINE "A"

Note: "P" in assembly line position indicates "Lead-Free"



OR

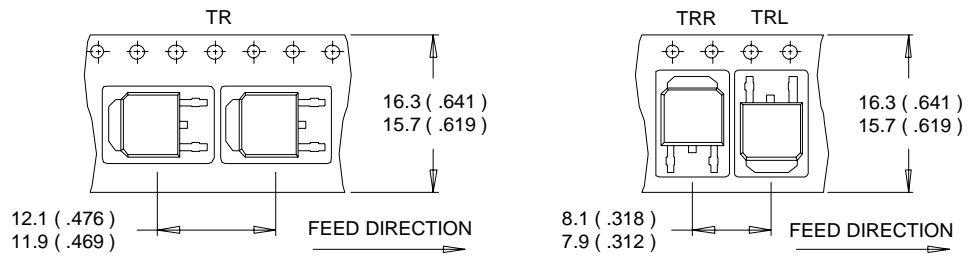


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International
IR Rectifier

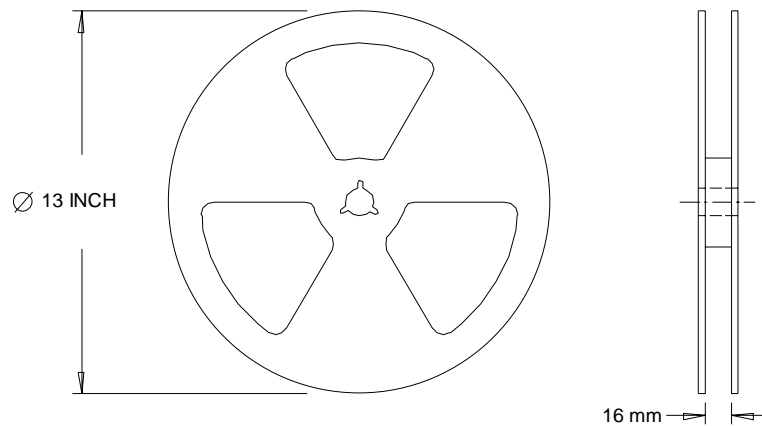
D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. OUTLINE CONFORMS TO EIA-481.

Data and specifications subject to change without notice.

International
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